Abstract:
Power factor correction apparatus, for a switching power supply fed by an array of rectifying diodes and consisting of at least an input inductor, a contact of which is connected in series with a contact of the array, and of a power switch connected between the other contact of the array and the other contact of the input inductor that comprises circuitry for identifying, in each cycle determined by the switching frequency of the power supply, whenever the instantaneous value of the current through the inductor reaches a minimal value; circuitry for switching the power switch to its conducting state in response to the minimal current through the inductor; circuitry for reflecting the current flowing through the inductor by a measurable or simulated parameter; and circuitry for providing indication, in each cycle, by using the parameter, the indication being related to the timing until the peak value of the current, that corresponds to a specific load, has been essentially reached, or to the time from the moment that the current reaches the minimal value until the timing, and for switching the power switch to its non-conducting state in response to the indication.