

A CHAOS MODEL OF SUBHARMONIC OSCILLATIONS IN CURRENT MODE PWM BOOST CONVERTERS

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ABSTRACT

Chaos concepts formulated in a discrete form were applied to examine instability conditions in a current mode PWM Boost converter under open and closed outer-loop conditions. A simple expression for the maximum duty cycle for subharmonic-free operation was developed and applied to assess the effects of the outer loop on subharmonic oscillations in the converter under study.

I. Introduction

Switch Mode systems are notorious for their potential to develop instability (by which we mean the onset of parasitic oscillations). A coarse examination of the nature of these instabilities suggests that they might have two distinct and possibly unrelated origins. One is associated with 'analog' instability that can be explained in terms of linear feedback theory. The second, a more devious one, is apparently associated with the sampling or discrete nature of switch mode systems. An example of the latter is the onset of sub-harmonic oscillations in current mode (CM) converters. These unstable conditions were recently explained in terms of a Chaos model [1-2] which seems to fit the nature of switch mode systems. Indeed, it has been shown [3] that subharmonic oscillations in CM is a manifestation of a chaotic behavior. This phenomena was originally explained [4] by considering the propagation of a disturbance in a CM controlled system. This fundamental explanation and its extension [5-6] are insufficient, though, to quantize subharmonic phenomena encountered in complex, closed loop systems such as reported in [7].

The objective of this study was to describe and explain by a Chaos model the behavior of a CM Boost converter under open and closed outer loop situations. The study was motivated by the feeling that a quantitative model can help to examine the effect of the outer loop

components on the onset of sub-harmonic oscillations and to quantize the nature of the oscillation in terms of harmonic content under open and closed outer loop conditions. Once developed the model can be used to examine other situations in which instability can be expected.

II. CHAOS MODEL OF A CURRENT MODE BOOST CONVERTER

The CM Boost converter considered in this study (Fig. 1) is based on the generic topology as described in [5]. It is assumed that the converter is operating in the continuous conduction mode. The circuit diagram of Fig. 1 serves as a reference for the three cases discussed below:

1. An open outer loop configuration with a constant control voltage (V_c) (solid line part) and no slope compensation ($m_c=0$).
2. An open outer loop with slope compensation.
3. A closed outer loop with no slope compensation.

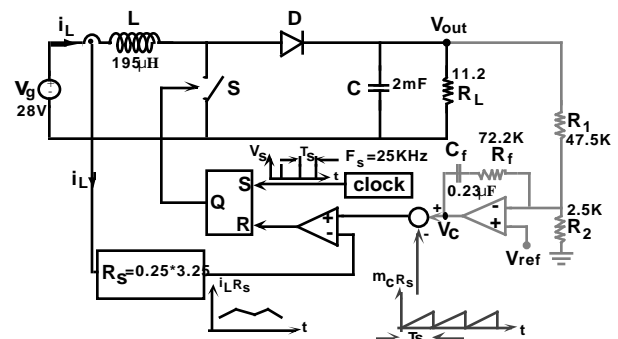


Fig. 1. Circuit diagram of the generic Current Mode controlled boost converter considered in this study.

The basic waveforms related to the current controlled programming (Fig. 2) include a current control signal V_c/R_s (referred to the inductor current), slopes m_1 and m_2

of the inductor current and a compensation slope m_c as normally added to ensure stability over the complete duty cycle range. The turn off instant occurs when the peak inductor current reaches the value of the control current in Fig 2(a) or the combined signal of V_c/R_s plus m_c (Fig. 2b)

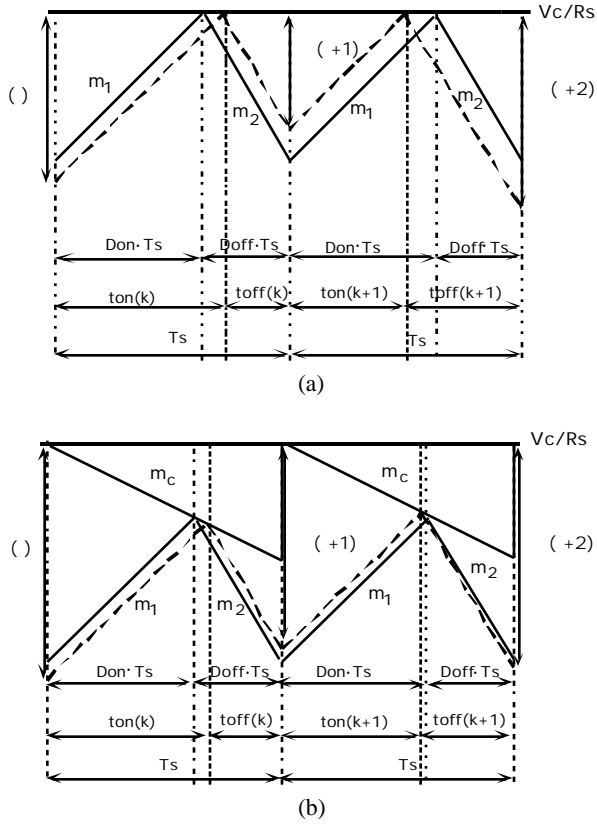


Fig. 2. Propagation of perturbation in inductor current, when V_c is const (opened outer loop). (a) without m_c . (b) with m_c

Examination of the waveform associated with the propagation of a perturbation over two cycles (Fig. 2, dashed-line) reveals that the deviations of inductor current (I) (at the beginning of each cycle) from the control signal V_c/R_s are related to other basic parameters (marked in Fig. 2b) by the following relationships:

$$I(k) = (m_1 + m_c) \text{ton}(k) \quad (1)$$

$$I(k+1) = m_2 \text{toff}(k) + m_c \text{ton}(k) \quad (2)$$

where k is the (discrete) cycle index and $\text{ton}(k)$ and $\text{toff}(k)$ represent the 'on' and 'off' time in a perturbed cycle k .

The discrete time difference equation of the system is thus:

$$I(k+1) = \frac{m_2 - m_c}{m_1 + m_c} I(k) + m_2 T_s \quad (3)$$

For stability we require:

$$\left| \frac{d I(k+1)}{d I(k)} \right| < 1 \quad (4)$$

And hence the stability criterion for the case under study can be expressed as:

$$\frac{m_2 - m_c}{m_1 + m_c} < 1 \quad (5)$$

In the absence of slope compensation, $m_c = 0$ and under steady-state conditions (Fig. 2(a) solid line):

$$I = m_1 \text{Don} T_s = m_2 \text{Doff} T_s \quad (6)$$

where all parameters refer to their steady-state values.

In this case (no slope compensation, open outer loop), (5) compresses to:

$$\frac{m_2}{m_1} = \frac{\text{Don}}{\text{Doff}} < 1 \quad (7)$$

which implies that stability is assured for $\text{Don} < 0.5$, as is well known.

For nonzero m_c , we can apply (5) to determine the minimum value of m_c required to ensure stability:

$$m_c > m_1 \left(\frac{1}{2\text{Doff}} - 1 \right) \quad (8a)$$

or

$$m_c > m_2 \left(1 - \frac{1}{2\text{Don}} \right) \quad (8b)$$

The relationship (3) can be used to develop the discrete map of $I(k+1) = f(I(k))$. This was accomplished by a MATLAB (MathWorks Inc.) subroutine that was run for a hundred cycles. As evident from Fig. 3(a), with $m_c = 0$ and $\text{Don} = 0.6$ the Boost converter Fig. 1 under open outer loop is unstable. In contrast, the single point of Fig. 3(b) implies stability for the same converter with a slope compensation.

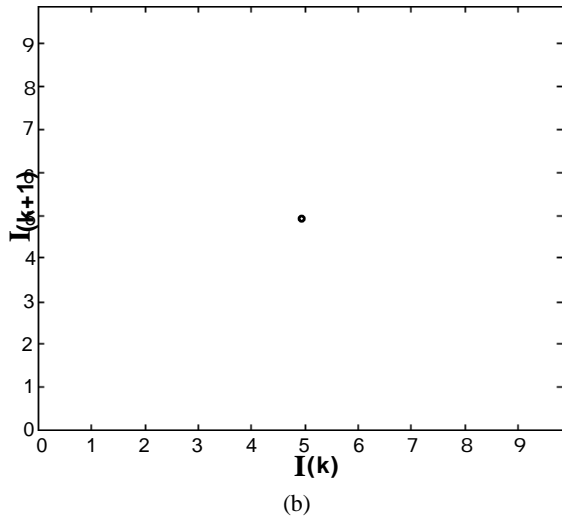
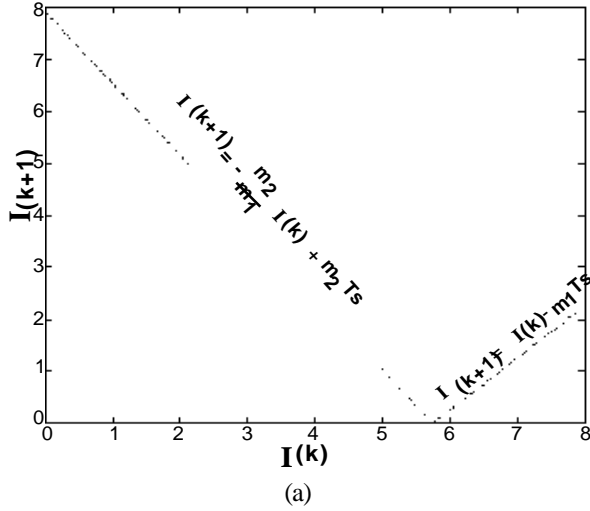


Fig. 3. A Discrete map of

I
Error!=Constant) conditions with (a) no slope compensation ($m_c=0$) (b) with slope compensation ($m_c =0.54 m_1$). Produced by MATLAB (MathWorks Inc.) for $Don=0.6$. Plots represent a sequence of one hundred cycles from $k=900$ to $k=1000$.

Another important instrument for examining and explaining the stability properties of a chaotic system is the bifurcation diagram [3]. Fig. 4(a) illustrates the creation of sub-harmonic oscillation ($Don > 0.5$) as function of duty cycle in open loop converter for a zero m_c . For nonzero m_c (Fig. 4(b)) the borderline between the stable and chaotic region moves to a higher duty cycle according of (8).

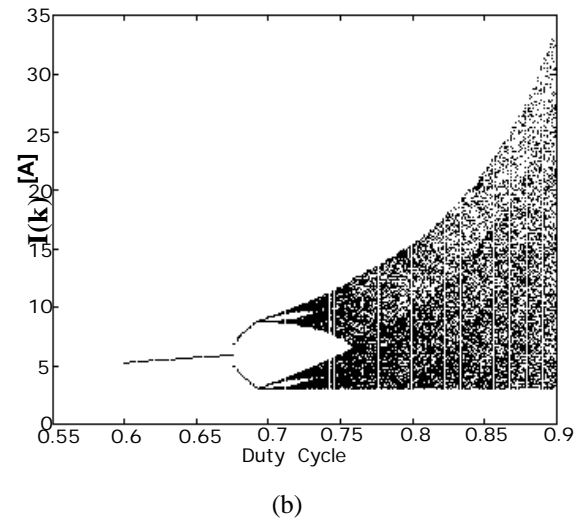
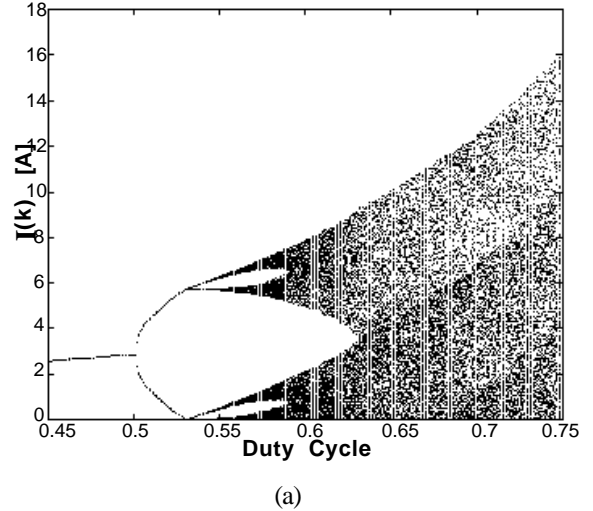


Fig. 4. Bifurcation diagram produced by sweeping the duty cycle parameter (a) over the range 0.45 to 0.75 and $m_c=0$. (b) over the range 0.6 to 0.9 and $m_c = 0.54 m_1$.

III. THE EFFECT OF THE OUTER VOLTAGE FEEDBACK LOOP

The inductor current waveform under closed outer voltage loop conditions is shown in Fig. 5 (refer to Fig. 1 for notations). The solid lines represent the steady-state condition whereas the dashed line shows a perturbed waveforms of the inductor current.

In Fig. 5 the reference current is denoted I_{ref} corresponding to V_c/R_s in the open loop case (Fig. 2(a)). The slopes m_{c1} and m_{c2} are an approximation of the instantaneous rising and falling portions of the control voltage (V_c), scaled by the current feedback network (R_s).

Unlikely the case of the open outer loop situation, the voltage control (V_c) in the closed outer loop system is affected by the output voltage ripple and therefore is not a constant even at steady state. Furthermore, under sub-harmonic oscillation conditions V_c could be highly variable.

The interception point $i_{top}(k)$ (Fig. 5) of the slopes m_1 and m_{c1} can be obtain from simple geometrical relationships:

$$\begin{aligned} I(k) &= (m_1 - m_{c1}) \text{Don} Ts = \\ &= (m_2 - m_{c2}) \text{Doff} Ts = I(k+1) \end{aligned} \quad (9)$$

Consequently,

$$\frac{m_2 - m_{c2}}{m_1 - m_{c1}} = \frac{\text{Don}}{\text{Doff}} = \frac{m_2}{m_1} \quad (10)$$

and

$$I_L = I + I_{mc} \quad (11)$$

Under steady state conditions, I_L and I_{mc} are the ripple of inductor current and control voltage (V_c) respectively, scaled by R_s .

Under perturbed conditions (dashed-line):

$$I(k) = (m_1 - m_{c1}) \text{ton}(k) \quad (12)$$

$$I(k+1) = (m_2 - m_{c2}(k)) \text{toff}(k) \quad (13)$$

Which can be transformed into the difference equation:

$$I(k+1) = \frac{m_2 - m_{c2}(k)}{m_1 - m_{c1}} I(k) + (m_2 - m_{c2}(k)) Ts \quad (14)$$

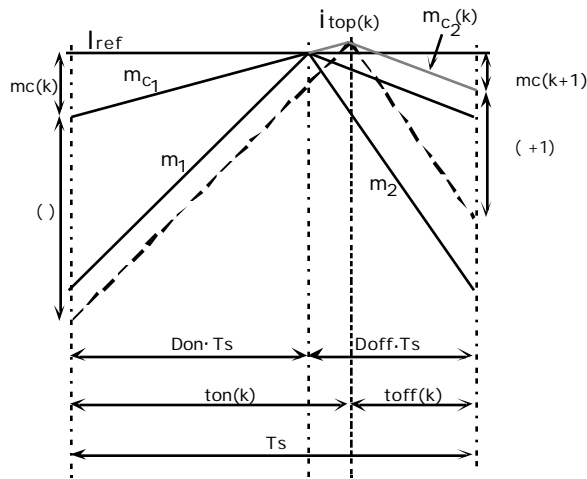


Fig. 5. Propagation of a perturbation in inductor current when the outer loop is closed and $m_c = 0$.

A first order approximation of m_{c1} and $m_{c2}(k)$ was obtained by deriving the analytical expression for V_{out} (Fig. 1) and applying Taylor series expansion. It was found that the scaled (by R_s) slopes can be expressed as:

$$m_{c1} = \frac{V_{out} R_f}{R_1 R_s} \quad (15)$$

$$\begin{aligned} m_{c2}(k) &= \frac{V_g R_f}{R_1 R_s} \frac{d^2 \text{toff}(k)}{2} + \frac{\text{toff}(k)}{(2)^2} - \frac{d^2 \text{toff}(k)}{4} \\ &+ \frac{V_g R_f}{\text{Doff} R_1 R_s} - \frac{1}{2} - \frac{d^2 \text{toff}(k)}{2} + \frac{\text{toff}(k)}{(2)^2} + \frac{d^2 \text{toff}(k)}{4} \\ &+ \frac{i_{top}(k) R_f}{C R_1 R_s} \left(1 - \frac{\text{toff}(k)}{2} \right) \end{aligned} \quad (16)$$

where

$$= R_L C \quad (17)$$

$$d = \sqrt{\frac{1}{LC} - \frac{1}{(2)^2}} \quad (18)$$

$$i_{top}(k) = I_{ref} - (I(k) + I_{mc}(k)) + m_1 \text{ton}(k) \quad (19)$$

$$I_{ref} = \frac{V_g}{\text{Doff}^2 R_L} + \frac{V_g \text{Don} Ts}{2 L} \quad (20)$$

Applying the above, the borderline Doff between the stable and unstable regions was derived to be

$$\begin{aligned} \text{Doff}^4 &= \frac{d^2 Ts}{2} - \frac{Ts}{4LC} + \\ \text{Doff}^3 &= -\frac{d^2}{2} - \frac{1}{(2)^2} - \frac{d^2 Ts}{2} - \frac{R_f Ts}{2 R_1 R_s C} + \frac{Ts}{4LC} \\ &+ \text{Doff}^2 = \frac{1}{(2)^2} + \frac{d^2}{2} + \frac{R_f}{R_1 R_s C} - \frac{2 R_s R_1}{L Ts R_f} + \\ \text{Doff} &= \frac{1}{2} + \frac{2}{Ts} + \frac{R_s R_1}{L Ts R_f} - \frac{1}{Ts} = 0 \end{aligned} \quad (21)$$

The importance of this expression is its ability to predict the minimum Doff for sub-harmonic-free operation for the Boost converter. It should be noted that the polynomial equation is only a function of the converter's components' values and switching frequency. For example, for the nominal values of Fig. 1 [5], the

limit duty cycle before sub-harmonic develops is $D_{off}=0.546$ or $D_{on}=0.454$. Changing C to $1mF$ will move the limit point significantly to $D_{off}=0.686$ or $D_{on}=0.314$.

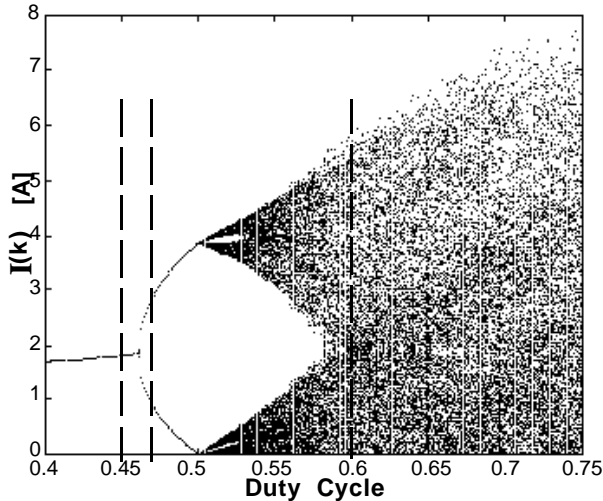


Fig. 11. Bifurcation diagram for the Boost converter of Fig. 1 under closed outer loop conditions. $F_s=25KHz$.

IV. DISCUSSION AND CONCLUSIONS

The results of this study clearly show that sub-harmonic oscillation in CM converters can readily be explained by the Chaos model developed in this investigation. The model was verified against exact circuit simulation and was found to predict faithfully the behavior of a CM Boost converter under various operating conditions. The D_{on} of 0.5, which is often quoted as the borderline for sub-harmonic-free zone is correct for open outer loop conditions. When the outer loop is closed, the borderline might move significantly to rather low D_{on} values. This implies that when slope compensation is not used a D_{on} (max) of 0.5 is no guarantee for stability. When compensation slope is applied, stability is assured only if the slope is adjusted according to the criterion which takes into account the effect of the outer loop.

An examination of the expressions developed in this study reveals that the stability boundary is effected by the major power components (main inductor, output capacitor and load) and the voltage feedback network. The main conclusions are summarized as follows:

1. A decrease in the values of the switching frequency (F_s), output capacitor (C) and/or load resistor (R_L), will lower the D_{on} limit for subharmonic-free operation in a current mode Boost converter.
2. An increase in the values of input inductor (L_{in}) and/or the high frequency gain of the outer loop (R_f/R_1), will lower the D_{on} limit for subharmonic-free operation in a current mode Boost converter.

3. A preliminary analysis shows that the behavior of a current mode flyback converter is rather similar to that of a Boost converter.

4. A cursory exploration suggests that a current mode Buck converter is less sensitive to the to the outer loop as far as subharmonic oscillations ar concerned. However, under some operating conditions the limit D_{on} is appreciably lower than 0.5.

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