**Features**

- High Current Drive Capability (200mA)
- Adjustable Duty Cycle
- Temperature Stability of 0.005%/°C
- Timing From μSec to Hours
- Turn off Time Less Than 2μSec

**Applications**

- Precision Timing
- Pulse Generation
- Time Delay Generation
- Sequential Timing

**Description**

The LM555/NE555/SA555 is a highly stable controller capable of producing accurate timing pulses. With a monostable operation, the time delay is controlled by one external resistor and one capacitor. With an astable operation, the frequency and duty cycle are accurately controlled by two external resistors and one capacitor.

**Internal Block Diagram**

![Internal Block Diagram](image-url)
## Absolute Maximum Ratings (TA = 25°C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>VCC</td>
<td>16</td>
<td>V</td>
</tr>
<tr>
<td>Lead Temperature (Soldering 10sec)</td>
<td>TLEAD</td>
<td>300</td>
<td>°C</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>PD</td>
<td>600</td>
<td>mW</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>TOPR</td>
<td>0 ~ +70</td>
<td>°C</td>
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<td>LM555/NE555</td>
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<td>-40 ~ +85</td>
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<td>SA555</td>
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<tr>
<td>Storage Temperature Range</td>
<td>TSTG</td>
<td>-65 ~ +150</td>
<td>°C</td>
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Electrical Characteristics

(TA = 25°C, VCC = 5 ~ 15V, unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
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<td>4.5</td>
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<td>V</td>
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<tr>
<td>Supply Current (Low Stable) (Note1)</td>
<td>ICC</td>
<td>VCC = 5V, RL = ∞</td>
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<td>3</td>
<td>6</td>
<td>mA</td>
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<tr>
<td></td>
<td></td>
<td>VCC = 15V, RL = ∞</td>
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<td>15</td>
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<td>Timing Error (Monostable) Initial Accuracy (Note2)</td>
<td>ACCUR</td>
<td>RA = 1kΩ to 100kΩ C = 0.1µF</td>
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<td>1.0</td>
<td>3.0</td>
<td>ppm%/°C</td>
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<tr>
<td>Drift with Temperature (Note4)</td>
<td></td>
<td></td>
<td>50</td>
<td>0.1</td>
<td>0.5</td>
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<tr>
<td>Drift with Supply Voltage (Note4)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>%/V</td>
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<tr>
<td>Timing Error (Astable) Initial Accuracy (Note2)</td>
<td>ACCUR</td>
<td>RA = 1kΩ to 100kΩ C = 0.1µF</td>
<td>-</td>
<td>2.25</td>
<td>-</td>
<td>ppm%/°C</td>
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<tr>
<td>Drift with Temperature (Note4)</td>
<td></td>
<td></td>
<td>150</td>
<td>0.3</td>
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<tr>
<td>Drift with Supply Voltage (Note4)</td>
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<td>%/V</td>
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<tr>
<td>Control Voltage</td>
<td>VC</td>
<td>VCC = 15V</td>
<td>9.0</td>
<td>10.0</td>
<td>11.0</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>VCC = 5V</td>
<td>2.6</td>
<td>3.33</td>
<td>4.0</td>
<td>V</td>
</tr>
<tr>
<td>Threshold Voltage</td>
<td>VTH</td>
<td>VCC = 15V</td>
<td>-</td>
<td>10.0</td>
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<td>V</td>
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<tr>
<td></td>
<td></td>
<td>VCC = 5V</td>
<td>-</td>
<td>3.33</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>Threshold Current (Note3)</td>
<td>ITH</td>
<td>-</td>
<td>-</td>
<td>0.1</td>
<td>0.25</td>
<td>µA</td>
</tr>
<tr>
<td>Trigger Voltage</td>
<td>VTR</td>
<td>VCC = 5V</td>
<td>1.1</td>
<td>1.67</td>
<td>2.2</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>VCC = 15V</td>
<td>4.5</td>
<td>5</td>
<td>5.6</td>
<td>V</td>
</tr>
<tr>
<td>Trigger Current</td>
<td>ITR</td>
<td>VTR = 0V</td>
<td>0.01</td>
<td>2.0</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>Reset Voltage</td>
<td>VRST</td>
<td>-</td>
<td>0.4</td>
<td>0.7</td>
<td>1.0</td>
<td>V</td>
</tr>
<tr>
<td>Reset Current</td>
<td>IRST</td>
<td>-</td>
<td>0.1</td>
<td>0.4</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Low Output Voltage</td>
<td>VOL</td>
<td>VCC = 15V ISINK = 10mA</td>
<td>-</td>
<td>0.06</td>
<td>0.25</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISINK = 50mA</td>
<td>-</td>
<td>0.3</td>
<td>0.75</td>
<td>V</td>
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<tr>
<td></td>
<td></td>
<td>VCC = 5V ISINK = 5mA</td>
<td>-</td>
<td>0.05</td>
<td>0.35</td>
<td>V</td>
</tr>
<tr>
<td>High Output Voltage</td>
<td>VOH</td>
<td>VCC = 15V ISOURCE = 200mA</td>
<td>12.75</td>
<td>12.5</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISOURCE = 100mA</td>
<td>13.3</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>VCC = 5V ISOURCE = 100mA</td>
<td>2.75</td>
<td>3.3</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>Rise Time of Output (Note4)</td>
<td>tR</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td>Fall Time of Output (Note4)</td>
<td>tF</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>ns</td>
</tr>
<tr>
<td>Discharge Leakage Current</td>
<td>IILKG</td>
<td>-</td>
<td>-</td>
<td>20</td>
<td>100</td>
<td>nA</td>
</tr>
</tbody>
</table>

Notes:
1. When the output is high, the supply current is typically 1mA less than at VCC = 5V.
2. Tested at VCC = 5.0V and VCC = 15V.
3. This will determine the maximum value of RA + RB for 15V operation, the max. total R = 20MΩ, and for 5V operation, the max. total R = 6.7MΩ.
4. These parameters, although guaranteed, are not 100% tested in production.
Application Information

Table 1 below is the basic operating table of 555 timer:

<table>
<thead>
<tr>
<th>Threshold Voltage (Vth)(PIN 6)</th>
<th>Trigger Voltage (Vtr)(PIN 2)</th>
<th>Reset(PIN 4)</th>
<th>Output(PIN 3)</th>
<th>Discharging Tr. (PIN 7)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Don't care</td>
<td>Don't care</td>
<td>Low</td>
<td>Low</td>
<td>ON</td>
</tr>
<tr>
<td>Vth &gt; 2Vcc / 3</td>
<td>Vth &gt; 2Vcc / 3</td>
<td>High</td>
<td>Low</td>
<td>ON</td>
</tr>
<tr>
<td>Vcc / 3 &lt; Vth &lt; 2 Vcc / 3</td>
<td>Vcc / 3 &lt; Vth &lt; 2 Vcc / 3</td>
<td>High</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Vth &lt; Vcc / 3</td>
<td>Vth &lt; Vcc / 3</td>
<td>High</td>
<td>High</td>
<td>OFF</td>
</tr>
</tbody>
</table>

When the low signal input is applied to the reset terminal, the timer output remains low regardless of the threshold voltage or the trigger voltage. Only when the high signal is applied to the reset terminal, the timer's output changes according to threshold voltage and trigger voltage.

When the threshold voltage exceeds 2/3 of the supply voltage while the timer output is high, the timer's internal discharge Tr. turns on, lowering the threshold voltage to below 1/3 of the supply voltage. During this time, the timer output is maintained low. Later, if a low signal is applied to the trigger voltage so that it becomes 1/3 of the supply voltage, the timer's internal discharge Tr. turns off, increasing the threshold voltage and driving the timer output again at high.

1. Monostable Operation

![Monoatable Circuit](image)

![Resistance and Capacitance vs. Time delay(td)](image)

![Waveforms of Monostable Operation](image)
Figure 1 illustrates a monostable circuit. In this mode, the timer generates a fixed pulse whenever the trigger voltage falls below Vcc/3. When the trigger pulse voltage applied to the #2 pin falls below Vcc/3 while the timer output is low, the timer's internal flip-flop turns the discharging Tr. off and causes the timer output to become high by charging the external capacitor C1 and setting the flip-flop output at the same time.

The voltage across the external capacitor C1, VC1 increases exponentially with the time constant t=RA*C and reaches 2Vcc/3 at td=1.1RA*C. Hence, capacitor C1 is charged through resistor RA. The greater the time constant RAC, the longer it takes for the VC1 to reach 2Vcc/3. In other words, the time constant RAC controls the output pulse width.

When the applied voltage to the capacitor C1 reaches 2Vcc/3, the comparator on the trigger terminal resets the flip-flop, turning the discharging Tr. on. At this time, C1 begins to discharge and the timer output converts to low.

In this way, the timer operating in the monostable repeats the above process. Figure 2 shows the time constant relationship based on RA and C. Figure 3 shows the general waveforms during the monostable operation.

It must be noted that, for a normal operation, the trigger pulse voltage needs to maintain a minimum of Vcc/3 before the timer output turns low. That is, although the output remains unaffected even if a different trigger pulse is applied while the output is high, it may be affected and the waveform does not operate properly if the trigger pulse voltage at the end of the output pulse remains at below Vcc/3. Figure 4 shows such a timer output abnormality.

2. Astable Operation

Figure 5. Astable Circuit

Figure 6. Capacitance and Resistance vs. Frequency
An astable timer operation is achieved by adding resistor \( R_B \) to Figure 1 and configuring as shown on Figure 5. In the astable operation, the trigger terminal and the threshold terminal are connected so that a self-trigger is formed, operating as a multivibrator. When the timer output is high, its internal discharging \( T_r \) turns off and the \( V_C1 \) increases by exponential function with the time constant \( (R_A+R_B)\cdot C \).

When the \( V_C1 \), or the threshold voltage, reaches \( 2V_{cc}/3 \), the comparator output on the trigger terminal becomes high, resetting the F/F and causing the timer output to become low. This in turn turns on the discharging \( T_r \) and the \( C1 \) discharges through the discharging channel formed by \( R_B \) and the discharging \( T_r \). When the \( V_C1 \) falls below \( V_{cc}/3 \), the comparator output on the trigger terminal becomes high and the timer output becomes high again. The discharging \( T_r \) turns off and the \( V_C1 \) rises again.

In the above process, the section where the timer output is high is the time it takes for the \( V_C1(t) \) to reach \( 2V_{cc}/3 \), and the section where the timer output is low is the time it takes for the \( V_C1 \) to drop from \( 2V_{cc}/3 \) to \( V_{cc}/3 \). When timer output is high, the equivalent circuit for charging capacitor \( C1 \) is as follows:

\[
V_{cc} \quad R_A \quad R_B \quad C1 \quad +V_c1(0-) = V_{cc}/3
\]

\[
C_1 \frac{dV_{c1}}{dt} = \frac{V_{cc} - V(0-)}{R_A + R_B} \quad (1)
\]

\[
V_{C1}(0+) = V_{CC}/3 \quad (2)
\]

\[
V_{C1(t)} = V_{CC} \left( 1 - \frac{2}{3} e^{-\left(\frac{t}{(R_A+R_B)C1}\right)} \right) \quad (3)
\]

Since the duration of the timer output high state \( (t_H) \) is the amount of time it takes for the \( V_C1(t) \) to reach \( 2V_{cc}/3 \),
The equivalent circuit for discharging capacitor $C_1$, when timer output is low is, as follows:

\[
V_{C_1}(t) = \frac{2}{3}V_{CC} = V_{CC} \left(1 - \frac{2}{3}e^{-\frac{t_H}{(R_A + R_B)C_1}}\right) \quad (4)
\]

\[
t_H = C_1(R_A + R_B)\ln 2 = 0.693(R_A + R_B)C_1 \quad (5)
\]

Since the duration of the timer output low state ($t_L$) is the amount of time it takes for $V_{C_1}(t)$ to reach $V_{CC}/3$,

\[
C_1 \frac{dv_{C_1}}{dt} + \frac{1}{R_A + R_B}V_{C_1} = 0 \quad (6)
\]

\[
V_{C_1}(t) = \frac{2}{3}V_{CC}e^{\frac{t_H}{(R_A + R_B)C_1}} \quad (7)
\]

Since $R_D$ is normally $R_B >> R_D$ although related to the size of discharging $T_r$, $t_L = 0.693R_BC_1$ (9)

Consequently, if the timer operates in astable, the period is the same with $T = t_H + t_L = 0.693(R_A+R_B)C_1+0.693R_BC_1 = 0.693(R_A+2R_B)C_1'$ because the period is the sum of the charge time and discharge time. And since frequency is the reciprocal of the period, the following applies.

\[
f = \frac{1}{T} = \frac{1.44}{(R_A + 2R_B)C_1} \quad (11)
\]

3. Frequency divider

By adjusting the length of the timing cycle, the basic circuit of Figure 1 can be made to operate as a frequency divider. Figure 8. illustrates a divide-by-three circuit that makes use of the fact that retriggering cannot occur during the timing cycle.
4. Pulse Width Modulation

The timer output waveform may be changed by modulating the control voltage applied to the timer’s pin 5 and changing the reference of the timer’s internal comparators. Figure 9 illustrates the pulse width modulation circuit. When the continuous trigger pulse train is applied in the monostable mode, the timer output width is modulated according to the signal applied to the control terminal. Sine wave as well as other waveforms may be applied as a signal to the control terminal. Figure 10 shows the example of pulse width modulation waveform.

5. Pulse Position Modulation

If the modulating signal is applied to the control terminal while the timer is connected for the astable operation as in Figure 11, the timer becomes a pulse position modulator. In the pulse position modulator, the reference of the timer’s internal comparators is modulated which in turn modulates the timer output according to the modulation signal applied to the control terminal. Figure 12 illustrates a sine wave for modulation signal and the resulting output pulse position modulation; however, any wave shape could be used.
6. Linear Ramp

When the pull-up resistor RA in the monostable circuit shown in Figure 1 is replaced with constant current source, the VC1 increases linearly, generating a linear ramp. Figure 13 shows the linear ramp generating circuit and Figure 14 illustrates the generated linear ramp waveforms.

\[
I_C = \frac{V_{CC} - V_E}{R_E} \quad (12)
\]

Here, \( V_E \) is

\[
V_E = V_{BE} + \frac{R_2}{R_1 + R_2} V_{CC} \quad (13)
\]

For example, if \( V_{CC}=15V \), \( R_E=20k\Omega \), \( R_1=5k\Omega \), \( R_2=10k\Omega \), and \( V_{BE}=0.7V \), \( V_E=0.7V+10V=10.7V \)

\( I_C=(15-10.7)/20k=0.215mA \)
When the trigger starts in a timer configured as shown in Figure 13, the current flowing through capacitor C1 becomes a constant current generated by PNP transistor and resistors. Hence, the $V_C$ is a linear ramp function as shown in Figure 14. The gradient $S$ of the linear ramp function is defined as follows:

$$S = \frac{V_p - V_p}{T} \quad (14)$$

Here the $V_{p-p}$ is the peak-to-peak voltage. If the electric charge amount accumulated in the capacitor is divided by the capacitance, the $V_C$ comes out as follows:

$$V = \frac{Q}{C} \quad (15)$$

The above equation divided on both sides by $T$ gives us

$$\frac{V}{T} = \frac{Q}{C} \quad (16)$$

and may be simplified into the following equation.

$$S = \frac{I}{C} \quad (17)$$

In other words, the gradient of the linear ramp function appearing across the capacitor can be obtained by using the constant current flowing through the capacitor. If the constant current flow through the capacitor is 0.215mA and the capacitance is 0.02$\mu$F, the gradient of the ramp function at both ends of the capacitor is $S = 0.215\text{m}/0.022\mu = 9.77\text{V}/\text{ms}$. 

\[ \text{LM555/NE555/SA555} \]
Mechanical Dimensions

Package Dimensions

Dimensions in millimeters

8-DIP

0.25 ±0.05
0.10 ±0.004
0.01 ±0.002

0.252 ±0.008

7.62
0.300

9.20 ±0.20
0.362 ±0.008

9.60 ±0.20
0.378 ±0.009

6.40 ±0.20
0.252 ±0.008

5.08 MAX
0.200

3.30 ±0.30
0.130 ±0.012

2.54
0.100

0.33
0.013 MIN

0.13 ±0.005
0.04 ±0.001

1.524 ±0.10
0.06 ±0.004

0.79 ±0.031

0.46 ±0.10
0.018 ±0.004

0.300 MAX

9.20 ±0.05
0.400 ±0.012

0.30
0.012

0.41
0.016

0.03
0.006

0.025
0.006
Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

8-SOP

![Diagram of 8-SOP package dimensions]
### Ordering Information

<table>
<thead>
<tr>
<th>Product Number</th>
<th>Package</th>
<th>Operating Temperature</th>
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<td>8-DIP</td>
<td>0 ~ +70°C</td>
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<tr>
<td>LM555CM</td>
<td>8-SOP</td>
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<td>8-DIP</td>
<td>0 ~ +70°C</td>
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<tr>
<td>NE555D</td>
<td>8-SOP</td>
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</tbody>
</table>

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<td>8-DIP</td>
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<td>SA555D</td>
<td>8-SOP</td>
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